

N32M016

N32M016	32 bit ARM Cortex-M0	64MHz	FLASH
64KB Flash	6KB SRAM	1	12bit 1Msps ADC
3	1	3	3
1 NTC	4	TIM	3*UART
2*I2C	2*SPI	3	DMA
6N	LDO	150V	

CPU

— 32 ARM Cortex-M0

— 64MHz

— 64KByte Main Flash 10 10

— 6KByte SRAM STOP

— Run

— Stop TIM6 IWDG UART3 COMP SRAM IO

— HSI_64M RC OSC 64MHz

— LSI RC OSC 32KHz

— 1 SYSCLK HSI

— 1 256

— # NTC

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— (PVD)

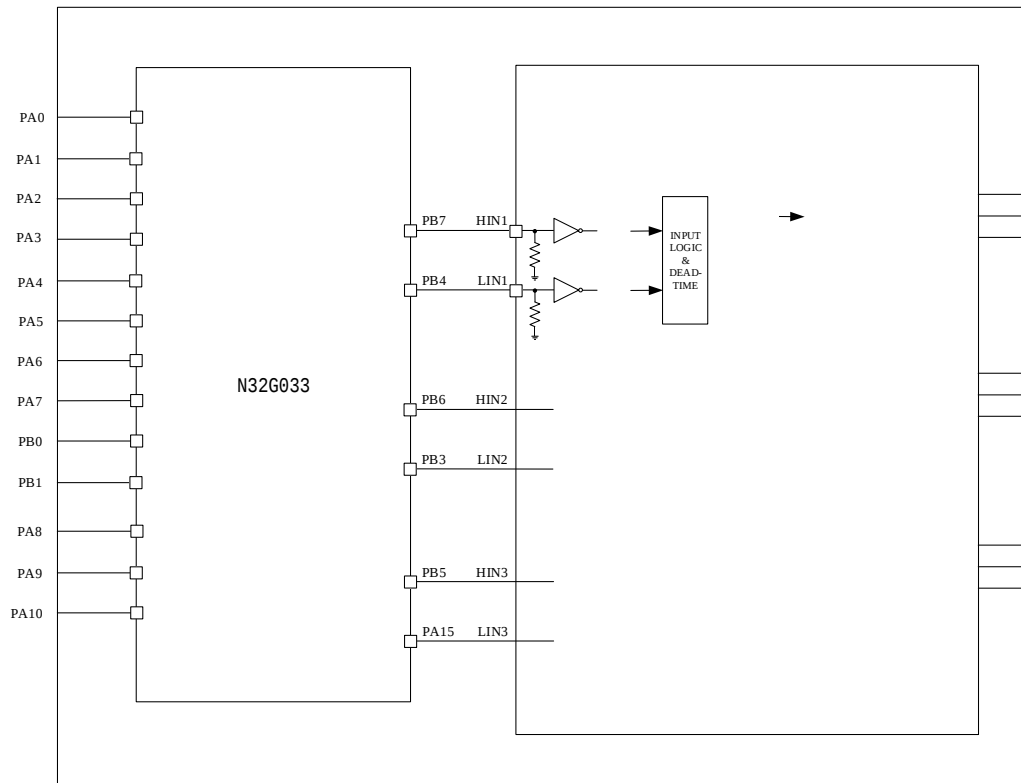
21 **GPIOs.**

— 1 16bit / 7

1

N32M016	N32G033x	MCU				N32G033			
N32G033		32	ARM Cortex [®] -M0			64MHz		64KB	
Flash	6KB SRAM		AHB			1	APB	29	
I/O			1	12	1Msps ADC	11		3	
	1					3	UART	2	I2C 2 SPI
N32G033		-40 ℃	+105 ℃			2.0V	5.5V		
					48				
1-1									

1-2 N32M016H8Q7

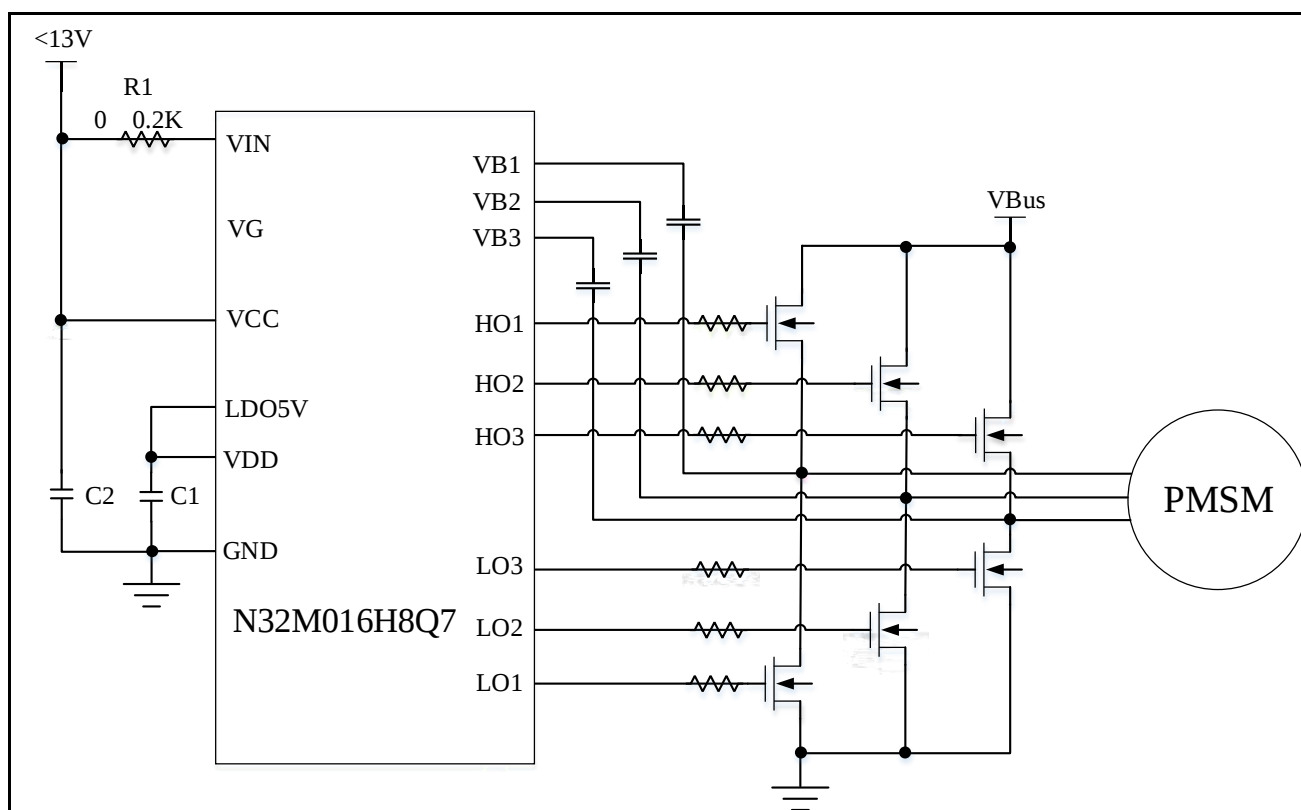


1.1

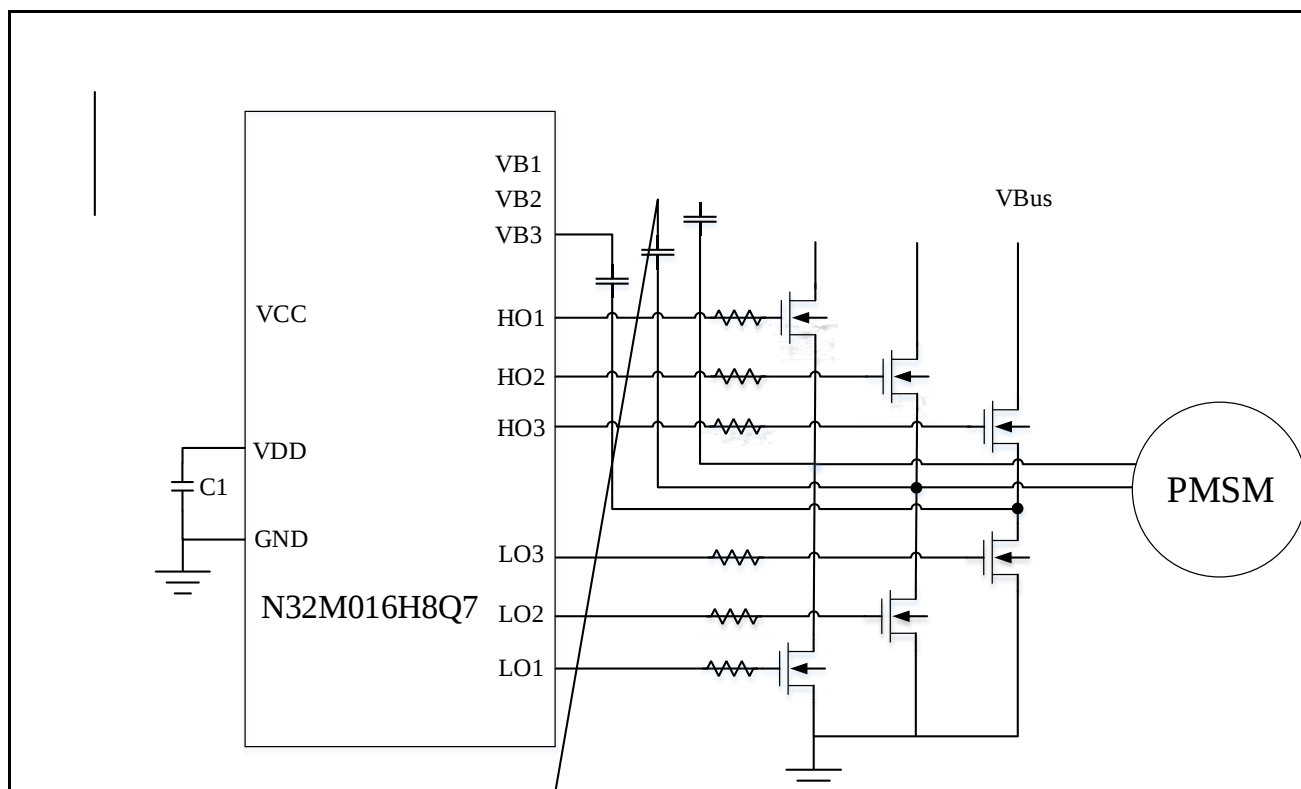
1-1 N32M016

		N32M016H8Q7
Flash KB		64
SRAM KB		6
CPU		ARM Cortex-M0 @64MHz
		2.0~5.5V/-40~105
		1
	16	1
	32	1
		1

1-3 N32M016H8Q7



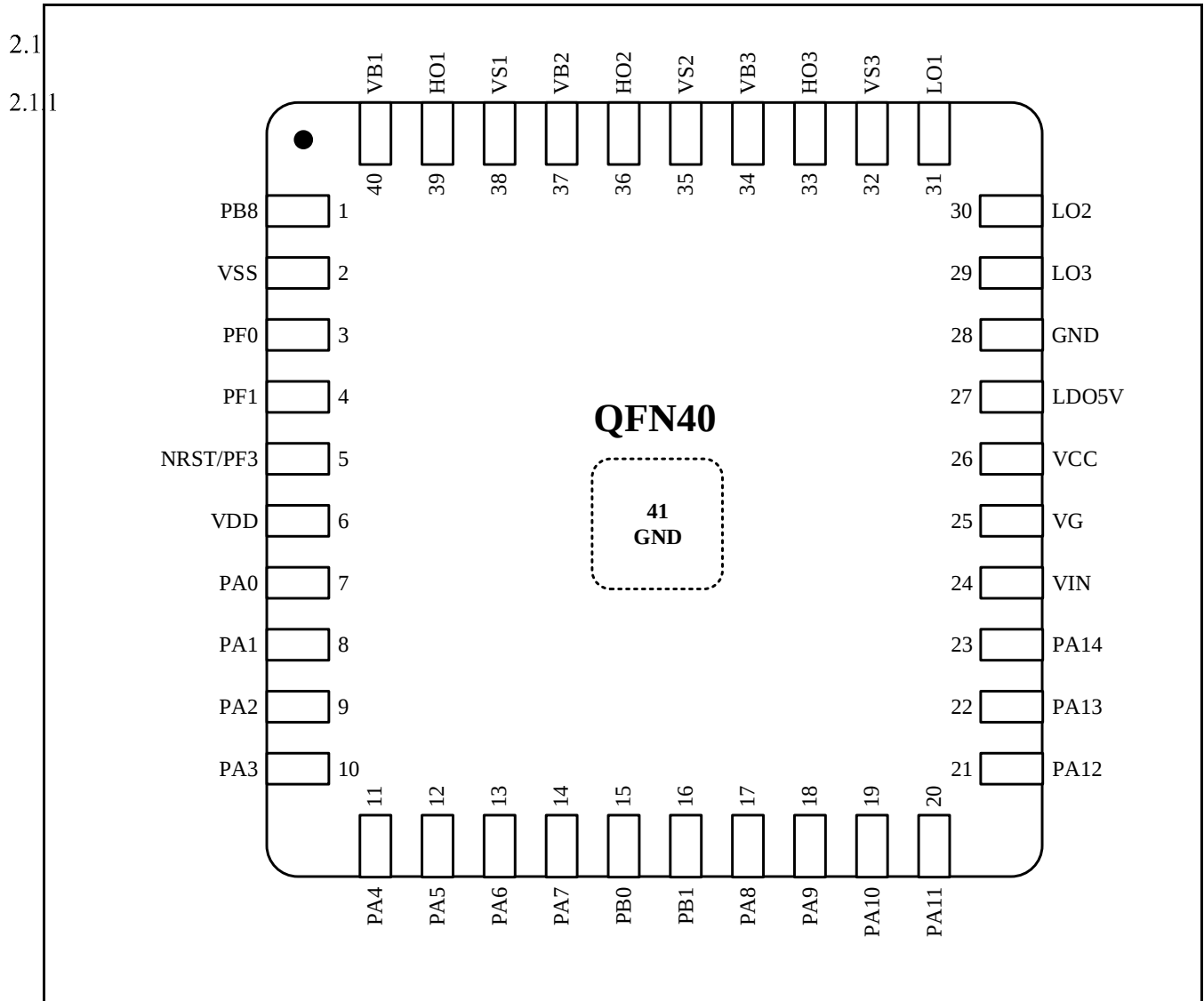
1-4 N32M016H8Q7



2

N32M016H8Q7

2-1 N32M016H8Q7 QFN40



9	PA2
10	PA3
11	PA4

12 PA

UART1_TX, UART2_TX, SPI1_MOSI, TIM1_BKIN, TIM3_CH3, TIM1_CH7, TIM4_CH3, UART3_TX,	ADC_IN2, OPAMP1_VINM,
UART1_RX, UART2_RX, TIM1_CH2, SPI1_MISO, UART3_RX	ADC_IN3, COMP_INP,
SPI1_MISO, TIM3_CH1, TIM1_CH1, SPI1_NSS, I2C1_SCL, UART3_TX, UART1_DE, UART2_DE,	ADC_IN4, COMP_INM, OPAMP1_VINP,

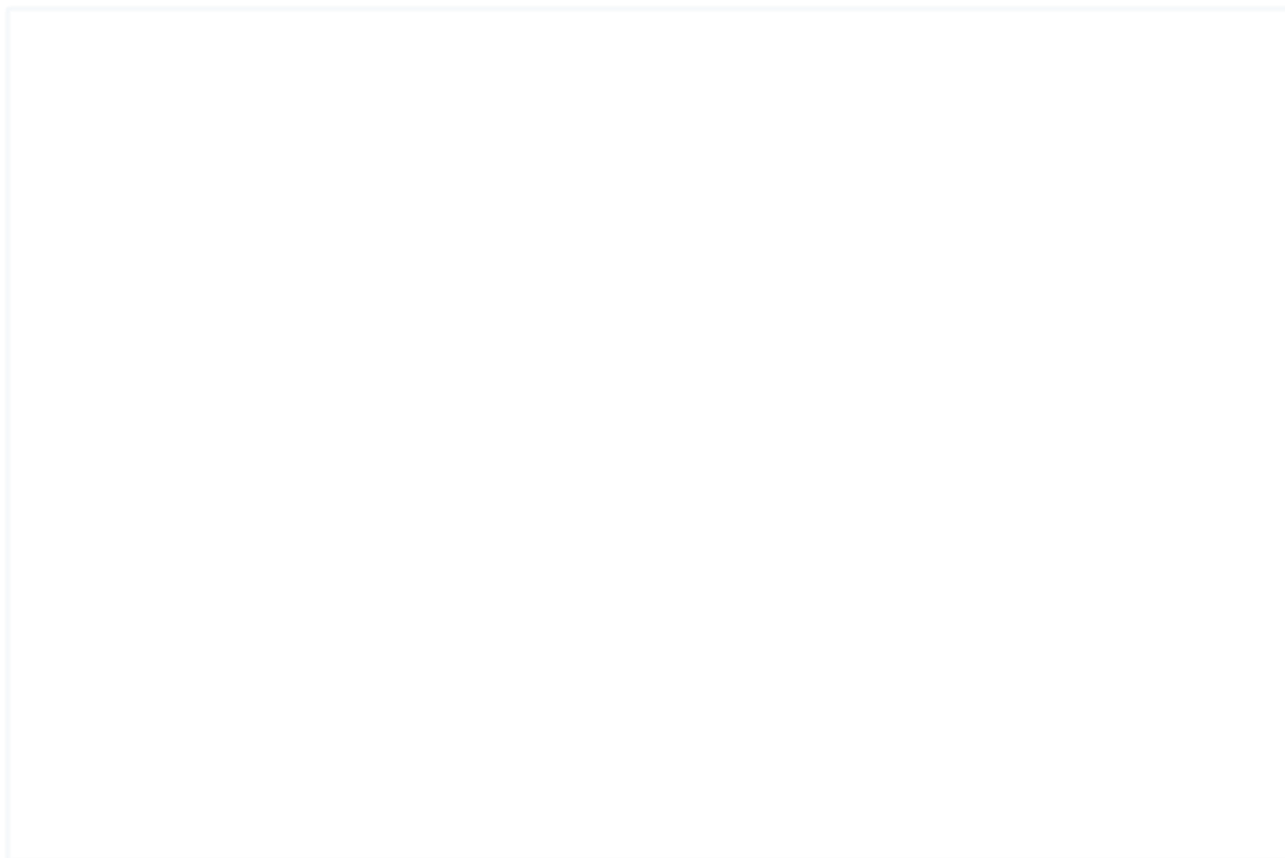
TIM1_CH3,

3

QFN40(5x5mm)

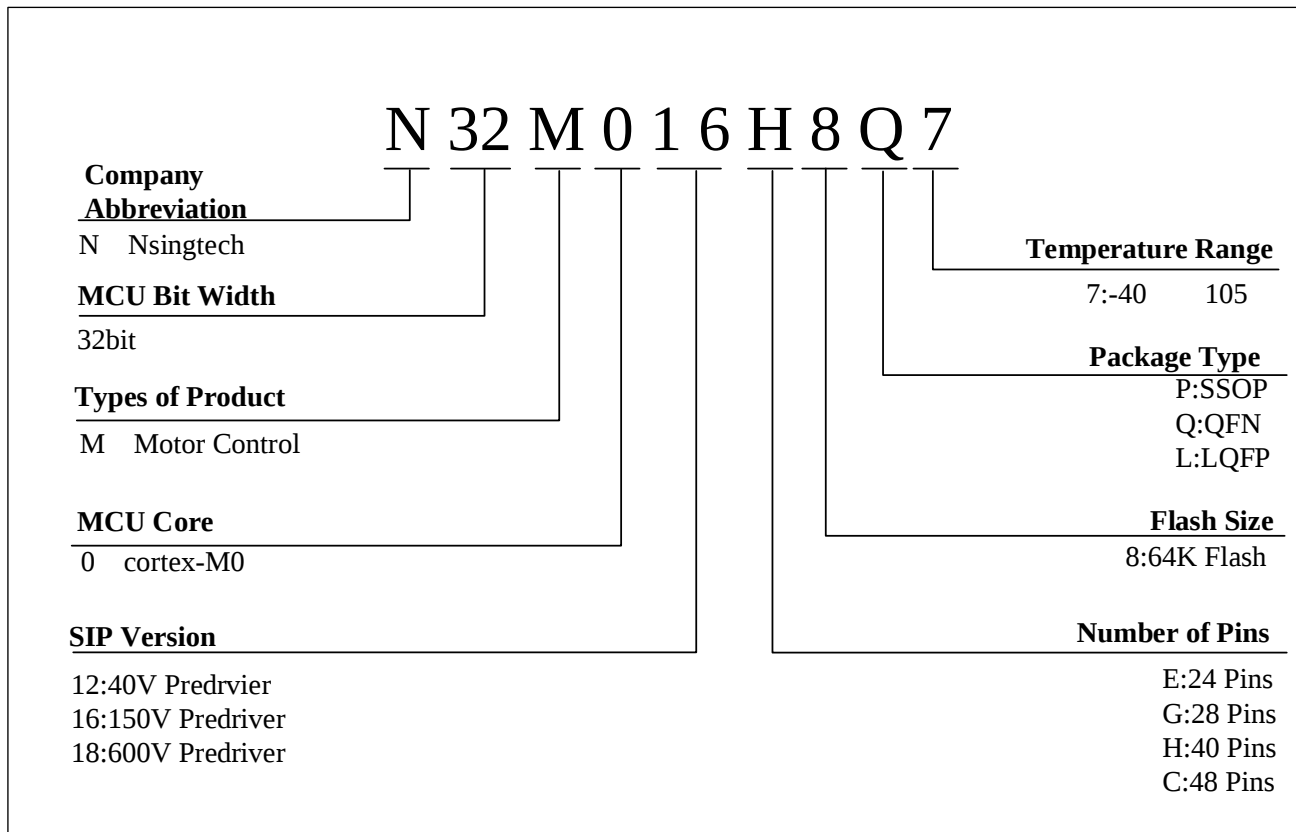
3-1 QFN40(5x5mm)

3.1



4

4-1 N32M016



4-1 N32M016

(1)			(2)	SPQ(3)	
N32M016H8Q7	QFN40	5mm *5mm	Tray	490	-40 ~105

- 1.
- 2.
- 3.

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2026.3.26	V1.0.0	1.
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